

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of:  
Robert L. Horn et al.

Application No.: 10/822,793

Confirmation No.: 3194

Filed: April 13, 2004

Art Unit: 2185

For: RAID CONTROLLER ARCHITECTURE  
WITH INTEGRATED MAP-AND-FORWARD  
FUNCTION, VIRTUALIZATION,  
SCALABILITY, AND MIRROR  
CONSISTENCY

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Examiner: J. U. Yu

**RESPONSE TO FINAL OFFICE ACTION**

MS AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated January 20, 2010, please consider the following remarks regarding the above-identified U.S. patent application.

**Remarks** begin on page 2 of this paper.

OK TO ENTER: /JY/ (05/03/2010)